Monte Dalrymple has been the primary designer of five generations of processors for Rabbit Semiconductor. Our partnership with Systemyde started in 1997 with the design of the original 8-bit Rabbit 2000; we had a long list of proposed features and a strict gate count requirement which Monte helped us meet by precise Verilog coding and careful analysis of the pros and cons of the feature set. He also helped us synthesize the design in an FPGA for hardware/software co-verification, as well as develop a full simulation test suite.

Each successive processor design built upon the previous design while adding new features, including new peripherals such as Ethernet and 802.11 WiFi, significant additions to the instruction set, and upgrading the design to a 16-bit internal bus. At the same time, the silicon technology used shrank from .65 micron down to 90nm, presenting new challenges with each generation. In each case, Monte worked closely with us to elaborate the details of our requests as well as propose alternate solutions to provide the same, or better, functionality. He also interacted with the foundries during the wafer fabrication process, providing valuable experience and insight to Rabbit.

Monte excelled at working remotely while keeping in full contact with Rabbit; the primary interface for all of the designs was remote with occasional face-to-face meetings. Monte has worked directly with design teams in California, Massachusetts, Maryland, and Taiwan to successfully integrate different portions of the most recent designs.

In short, Monte is a very accomplished, professional designer that I would not hesitate to use for any future design.

Lynn Wood
 ASIC Engineering Manager
Rabbit Semiconductor, Inc.
www.rabbit.com